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TRANSMITTAL OF FORMAL DRAWINGS				Docket No. BUR920000082US1 (13647)	
In Re Application Of: Wagdi W. Abadeer, et al.					
Application No. 09/811,915	Filing Date March 19, 2001	Examined Vinh P. Nguyen	Customer No. 23389	Group Art Unit 2829	Confirmation No. 8953
Invention: WAFER LEVEL SYSTEM FOR PRODUCING BURN-IN/SCREEN, AND RELIABILITY EVALUATIONS TO BE PERFORMED ON ALL CHIPS SIMULTANEOUSLY WITHOUT ANY WAFER CONTACTING					
Address to: Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450					
Transmitted herewith are: 8 sheets of formal drawing(s) for this application. <input checked="" type="checkbox"/> Each sheet of drawing indicates the identifying indicia suggested in 37 CFR Section 1.84(c).					
 Signature Steven Fischman Reg. No. 34,594 Scully, Scott, Murphy & Presser 400 Garden City Plaza - Suite 300 Garden City, New York 11530 (516) 742-4343			Dated: November 12, 2004 <div><input checked="" type="checkbox"/> I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to "Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450" [37 CFR 1.8(a)] on _____ (Date) _____ Signature of Person Mailing Correspondence _____ Typed or Printed Name of Person Mailing Correspondence</div>		